

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Nadeem N. Eleyan, et al.

Title: TEST CIRCUIT FOR MEASURING SENSE AMPLIFIER AND MEMORY MISMATCHES

Application No.: 10/683,636

Filed: October 10, 2003

Examiner: Not Yet Assigned

Group Art Unit: 2818

Atty. Docket No.: 004-30059

March 3, 2004

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

Pursuant to 1273 OG 55 (August 5, 2003), Information Disclosure Statements may be filed without copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003.

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action

by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

FEE AUTHORIZATION

- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that, on the date shown below, this correspondence is being

- ☒ deposited with the US Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
- ☐ facsimile transmitted to the US Patent and Trademark Office.

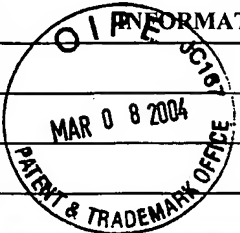
Nicole Teitler Cave 3/3/04
Nicole Teitler Cave Date

Respectfully submitted,

Nicole Teitler Cave

Nicole Teitler Cave, Reg. No. 54,021
Attorney for Applicant(s)
(512) 338-6315
(512) 338-6301 (fax)

EXPRESS MAIL LABEL: _____

U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 004-30059	
				Application No.: 10/683,636	
				Applicant(s): Nadeem N. Eleyan, et al.	
				Filing Date: October 10, 2003	
				Group Art Unit: 2818	
				Date Submitted: March 3, 2004	
U.S. Patent Documents					
*Examiner Initial		Document Number	Date	Name	
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AI				
	AK				
Foreign Patent Documents					
		Document	Date	Country	Translation
					Yes No
	AL				
	AM				
	AN				
	AP				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)					
	AR	ELEYAN, ET AL., U.S. Patent Application Publication entitled "Variable Delay Compensation for Data-Dependent Mismatch in Characteristic of Opposing Devices of a Sense Amplifier," Publication No. US 2003/0198112 A1, Published Oct. 23, 2003, pp – 12.			
	AS	ELEYAN, ET AL., U.S. Patent Application No. 10/074,396, entitled "Mechanism to Minimize Failure in Differential Sense Amplifiers," filed Feb. 11, 2002, pp. – 22.			
	AT	ELEYAN, ET AL., U.S. Patent Application No. 10/683,633, entitled "Measuring and Correcting Sense Amplifier and Memory Mismatches Using NBTI," filed Oct. 10, 2003, pp. – 31.			
Examiner			Date Considered		
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>					